

**Amendment and Response**

Applicant: Claus Dworski et al.

Serial No.: 10/564,650

Filed: August 7, 2006

Docket No.: I431.144.101/FIN503PCT/US

Title: ELECTRICAL CIRCUIT AND METHOD FOR TESTING ELECTRONIC COMPONENT

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**REMARKS**

The following remarks are made in response to the Non-Final Office Action mailed July 23, 2010. Claims 46 and 54-64 have been withdrawn from consideration. Claims 1-32 and 38 have been previously cancelled. Claims 39 and 43-45 have been allowed. Claims 40-42, 47 and 48 were rejected. Claims 33-37 and 49-53 have been objected to. With this Response, claims 40-42, 47 and 48 have been canceled, and claims 33, 35, 39, 49-51 and 53 have been amended. Claims 33-37, 39, 43-45 and 49-53 remain pending in the application and are presented for reconsideration and allowance.

**Drawings**

The Office Action objected to the drawings under 37 CFR 1.83(a). Referring to claim 51, the Office Action stated two testers must be shown. Claim 51 has been amended to recite *the* tester to further clarify only one tester is included in the claim. Regarding claim 39, the Office Action referred to “the multiplier receiving output of the adder.” Claim 39 has been amended to recite the multiplier receiving “the RMS value from the memory” which is illustrated in Figure 2.

The objections to the drawings are thus believed to be overcome.

**Claim Objections**

Claims 33, 35, 47, and 48 were objected to due to informalities. Claims 47 and 48 have been canceled. Claims 33 and 35 have been amended as suggested in the Office Action, thus overcoming the objections.

**Claim Rejections under 35 U.S.C. § 112**

Claims 47, 48, and 50-52 were rejected under 35 U.S.C. 112, second paragraph, as allegedly being indefinite. Claims 47 and 48 have been canceled. Claim 50 was rejected because “the electrical test circuit is not tied to the electrical phase displacement circuit.” Office Action at p. 5. Claim 50 has been amended to recite “wherein the

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first output of the phase displacement circuit is connected to the second input of the electrical test circuit” thus structurally connecting the two elements.

Claim 51 was rejected regarding the recitations of “the reference signal” and “the phase-displaced reference signal.” Claim 51 has been amended to clarify these elements.

As such, the rejections under section 112 are believed to be overcome.

**Claim Rejections under 35 U.S.C. § 102**

Claim 40 was rejected under 35 U.S.C. 102(b) as allegedly being anticipated by Miller (US 6,606,575). Claim 40 has been canceled.

**Claim Rejections under 35 U.S.C. § 103**

Claims 41, 42, 47, and 48 were rejected under 35 U.S.C. 103(a) as allegedly being unpatentable over Miller in view of Sacki (US 2002/0079938) and Utsumi et al. (US 5,029,289). Claims 41, 42, 47 and 48 have all been canceled.

**Allowable Subject Matter**

Claims 39 and 43-45 were allowed. The Examiner noted that claims 33-37 would be allowable if amended to overcome the objections set forth in the Office Action. Claims 33-37 have been so amended. Claims 49-53 were objected to for being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all limitations of the base claim and any intervening claims. Claims 49-51 and 53 have been amended to include the subject matter of original claim 40, thus placing them in independent form. Claim 52 depends on claim 51. Claims 49-53 are therefore all believed to be in condition for allowance.

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**CONCLUSION**

In view of the above, Applicant respectfully submits that all of the pending claims are in form for allowance. Therefore, reconsideration and withdrawal of the rejections and allowance of the claims are respectfully requested.

No fees are required under 37 C.F.R. 1.16(h)(i). However, if such fees are required, the Patent Office is hereby authorized to charge Deposit Account No. 50-0471.

Please consider this a Petition for Extension of Time for a sufficient number of months to enter these papers, if appropriate. At any time during the pendency of this application, please charge any additional fees or credit overpayment to Deposit Account No. 500471.

The Examiner is invited to contact the Applicant's representative at the below-listed telephone numbers to facilitate prosecution of this application.

Any inquiry regarding this Amendment and Response should be directed to Mark L. Gleason at Telephone No. (612) 767-2503, Facsimile No. (612) 573-2005. In addition, all correspondence should continue to be directed to the following address:

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Respectfully submitted,

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